



XA-9574

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Hideki YASUOKA et al.

Serial No.: 09/989,061

Group Art Unit: 2812

Filed: November 21, 2001

Examiner: J. Kennedy

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND METHOD  
OF MANUFACTURING THE SAME

\* \* \*

RESPONSE

Commissioner for Patents  
Washington, D.C. 20231

Sir:

Responding to the Office Action mailed August 5, 2002 and to the requirement for restriction therein, applicants elect, without traverse, Group I (Claims 1-39 and 59-60), reserving the right to file a divisional application with regard to the invention of Group II.

An action on the merits of the claims to the elected invention is respectfully requested.

The Commissioner is hereby authorized to charge to Deposit Account No. 50-1165 any fees under 37 C.F.R. §§1.16 and 1.17 which may be required by this paper, and to credit any overpayment to that Account. If any extension of time

is required in connection with the filing of this paper and  
has not been requested separately, then such extension is  
hereby requested.

Respectfully submitted,

By: Nelson H. Shapiro  
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NHS:lat

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